

OPERATION OF MODEL 730

ARC Mode

Timing is derived from the lower level threshold, which is set as low as possible. Time walk is minimal here. To be declared valid, pulse amplitude must exceed ULT by the time the "DELAY" times out. This eliminates false triggering caused by noise spikes.

DE Mode

The ULT is set to operate in anti-coincidence mode. To give an output, the signal must have passed the LLT, but must not have exceeded the ULT by the time the "DELAY" times out.

LE Mode

Operates as a conventional leading edge discriminator, using the level set by LLT. ULT is not used.

